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*Teri McDonald* 9/1/05

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Re Applic of	Hiroyuki Akatsu, et al.
Docket No.	FIS920030411US1
Serial No.	10/708,340
Filing Date	2/25/04
Attorney	H. Daniel Schnurmann

Attached: Response to Restriction Requirement

**PLEASE DELIVER TO: Dao H. Nguyen**  
**EXAMINER: ART UNIT: 2818**  
**CONFIRMATION NO.: 2339**  
**PHONE NO:**  
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## INTERNATIONAL BUSINESS MACHINES CORPORATION

Intellectual Property Law  
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I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING SENT VIA Facsimile Transmission to: COMMISSIONER OF  
PATENTS AND TRADEMARKS, Alexandria VA. 22313, on September 1, 2005 by: Teri McDonald

  
Signature & Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: <b>Horoyuki Akatsu, et al.</b>	Date: <b>September 1, 2005</b>
Serial Number: <b>10/708,340</b>	Examiner: <b>Dao H. Nguyen</b>
Filed: <b>02/25/2004</b>	Group Art Unit: <b>2818</b>
Title: <b>Structure and Method of Self-Aligned Bipolar Transistor Having Tapered Collector</b>	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner of Patents and Trademarks  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action dated August 24, 2005.

The Examiner in the aforementioned Office Action has required restriction under  
35 U.S.C. § 121, stating that the claims belong to:


GROUP I, Claims 1-10, drawn to semiconductor devices, and

GROUP II, Claims 11-20, drawn to a method of manufacturing semiconductor  
devices.

Applicants elect to prosecute the invention of GROUP I, consisting of Claims 1-10 drawn to semiconductor devices, and withdraw from consideration the claims forming GROUP II, as being drawn to non-elected invention, without prejudice to the Applicants' right to file a Divisional or Continuation or Continuation-in-Part patent application for the withdrawn claims.

The election is made without traverse.

Respectfully submitted,  
HORUYUKI AKATSU, ET AL.

By:   
H. Daniel Schnurmann, Agent  
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